

Fig. 1

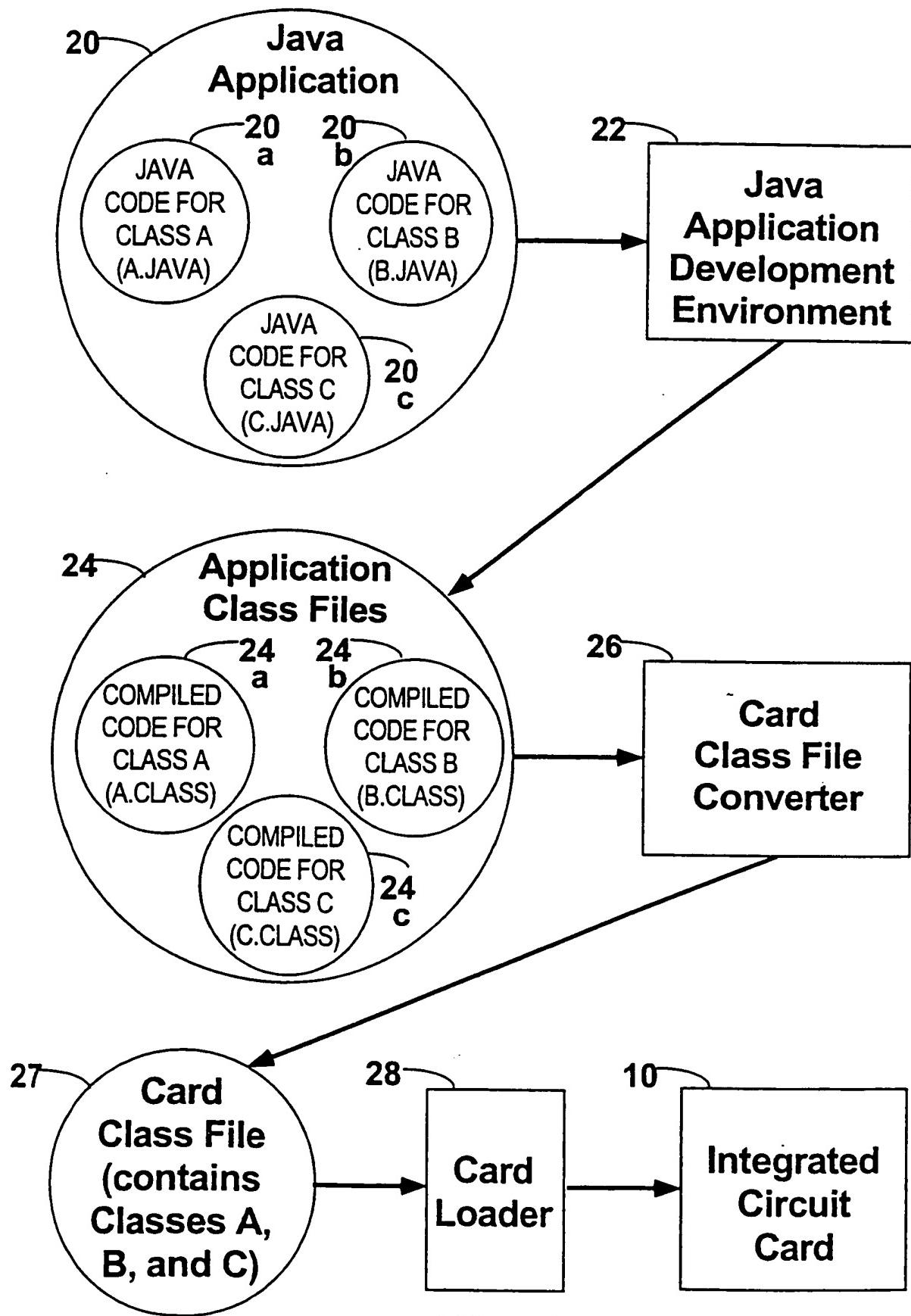


Fig. 2

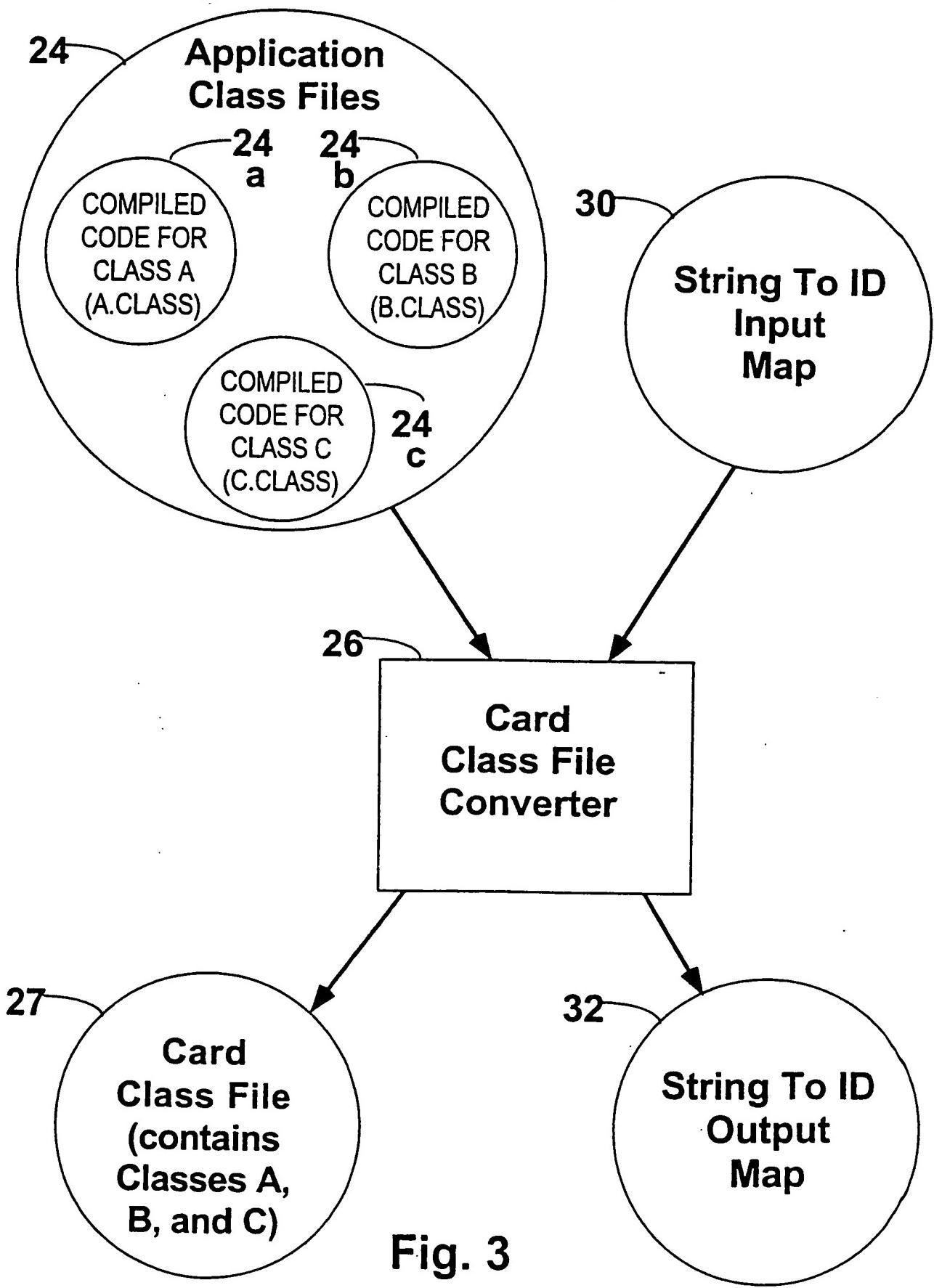
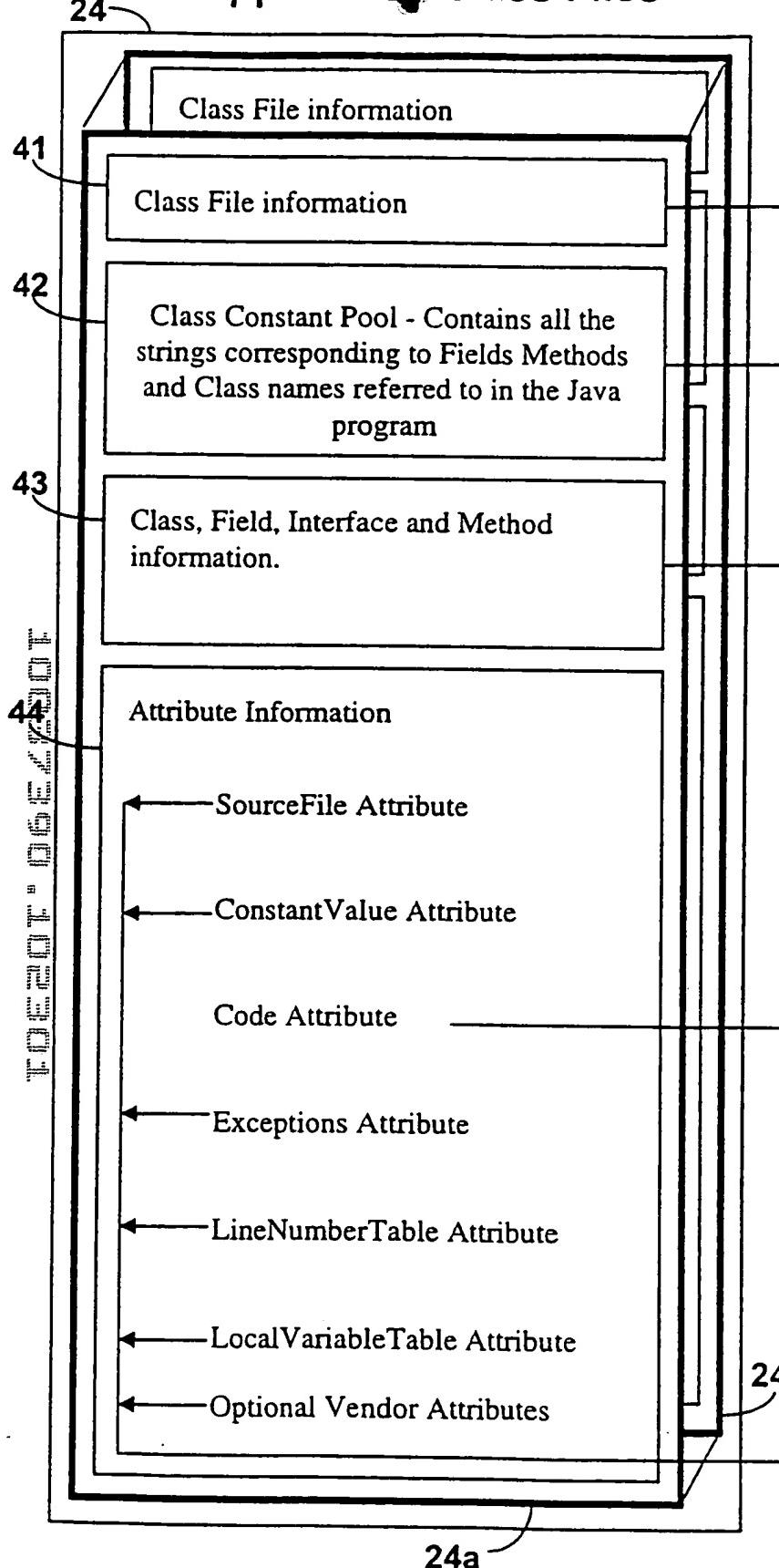


Fig. 3

Application Class Files



Card Class File

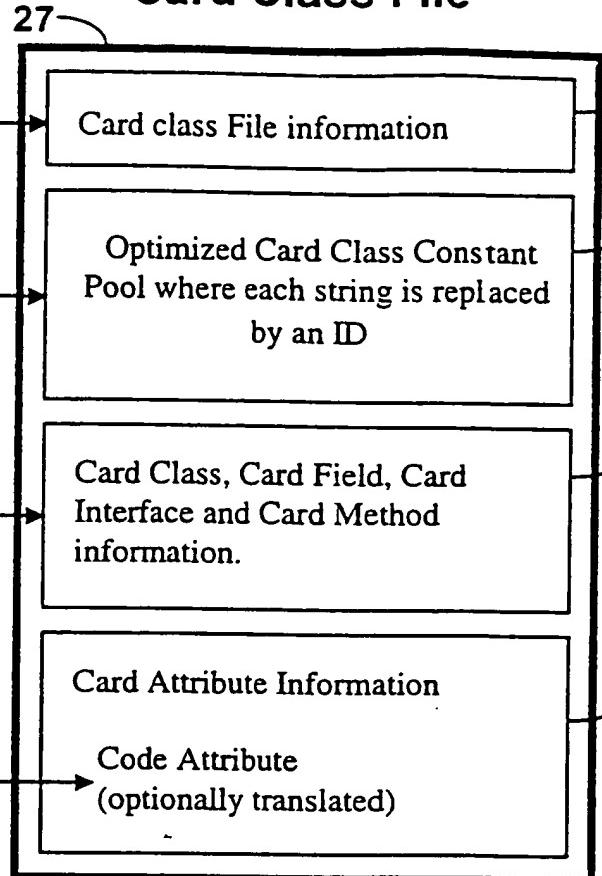


Fig. 4

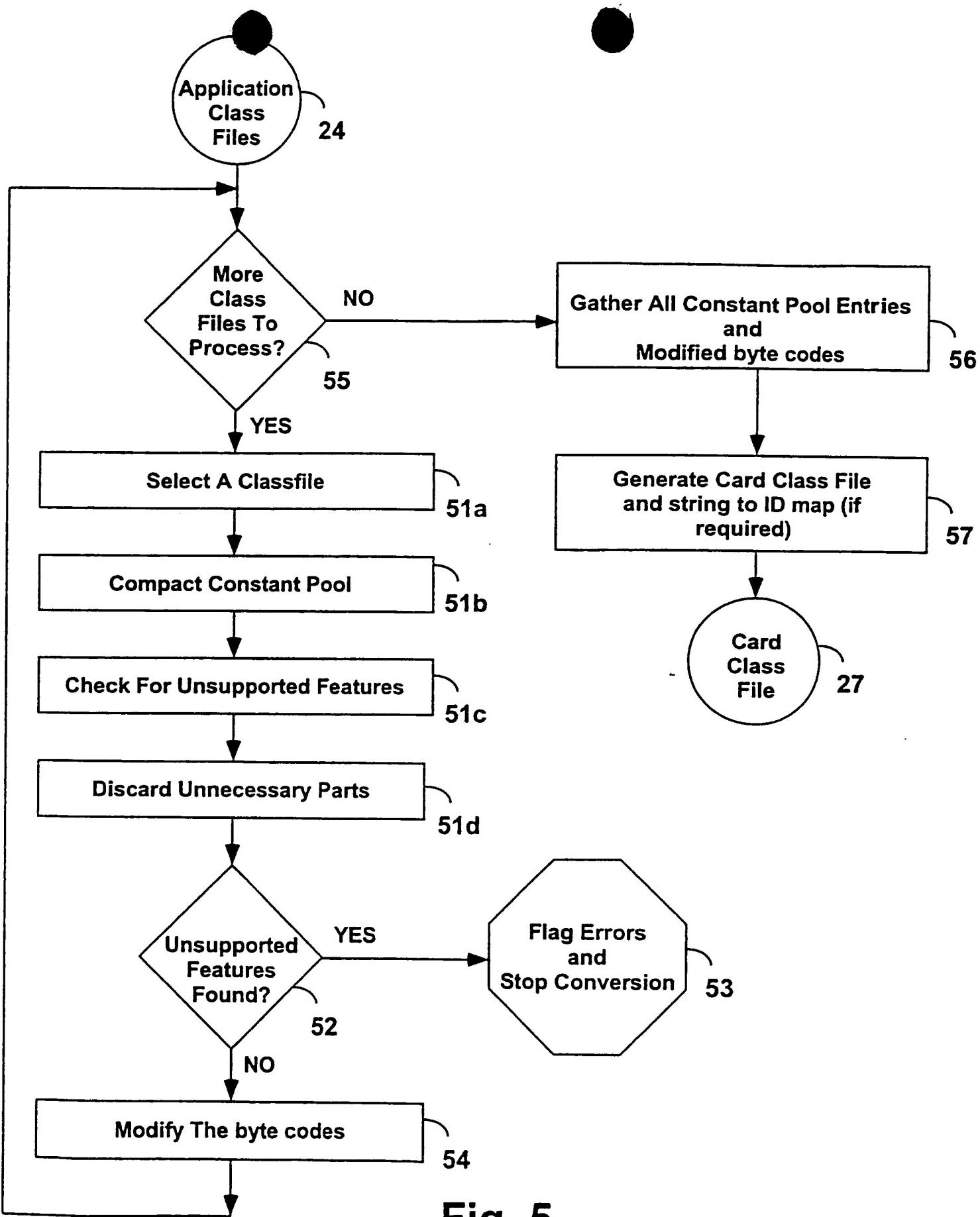


Fig. 5

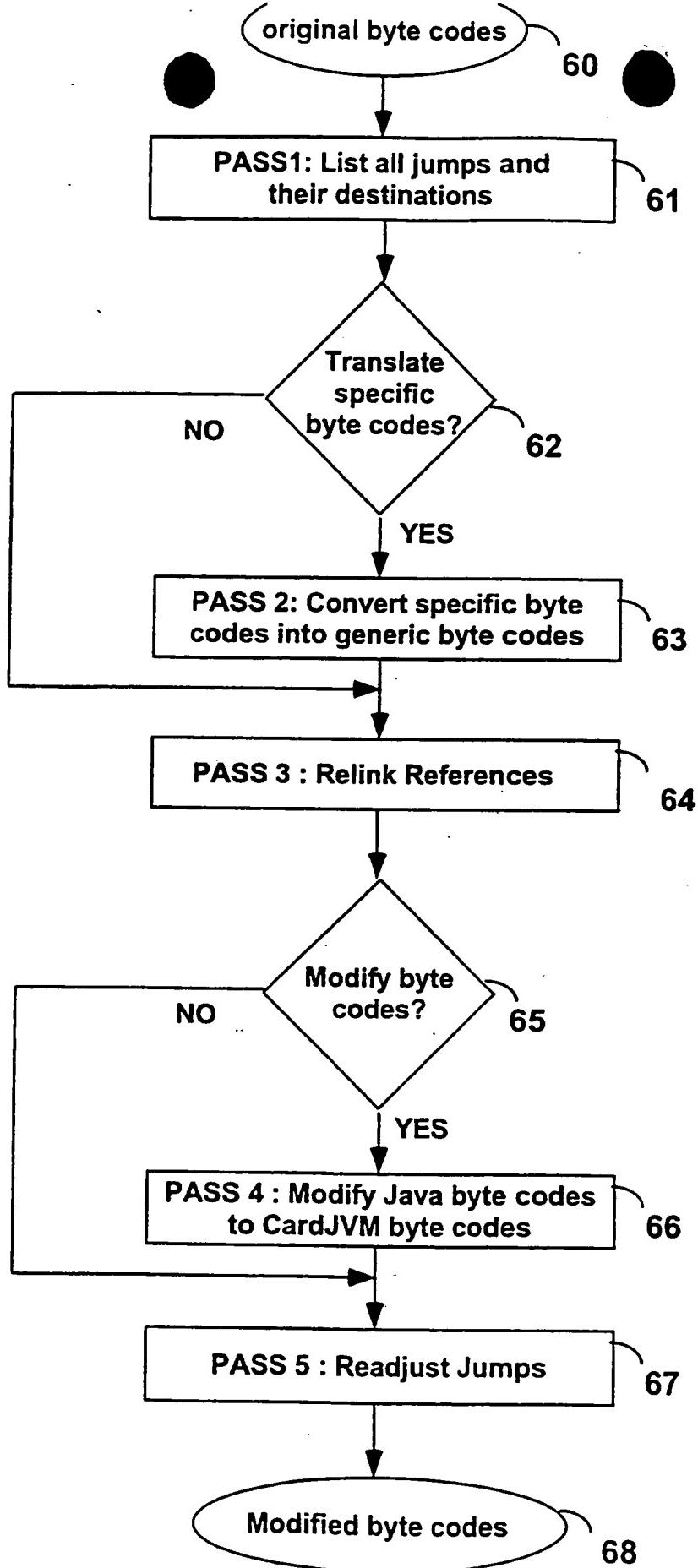


Fig. 6

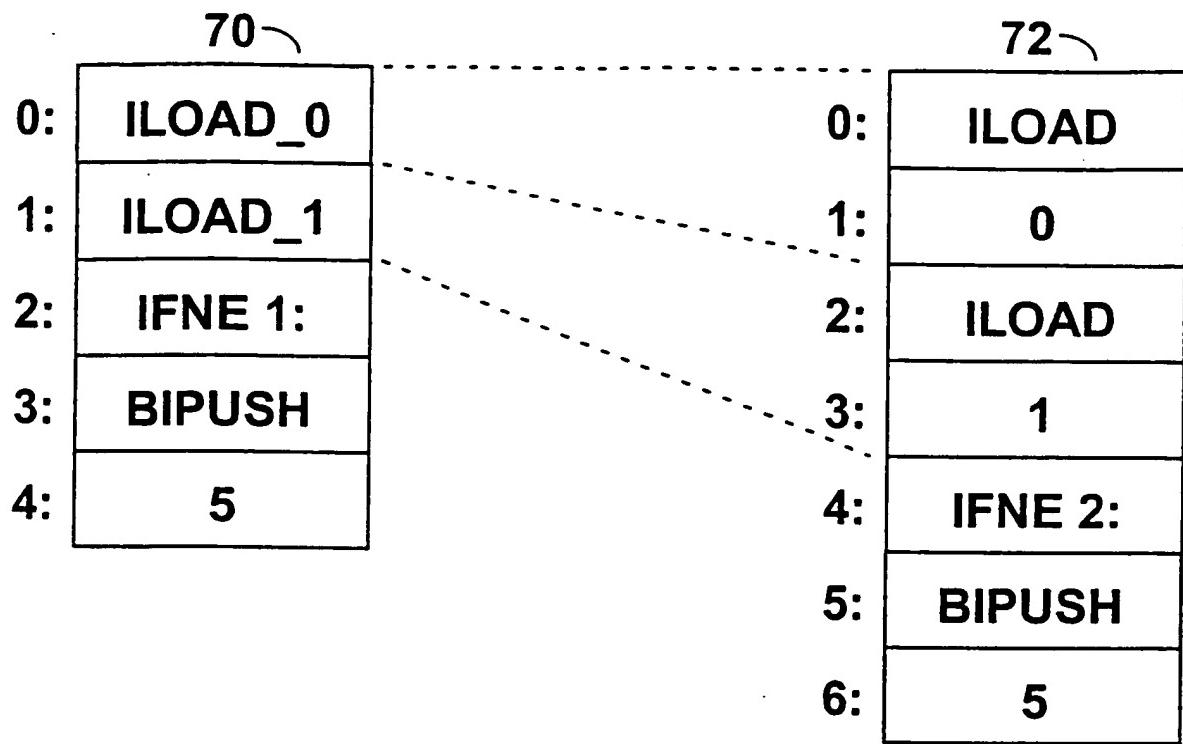


Fig. 7

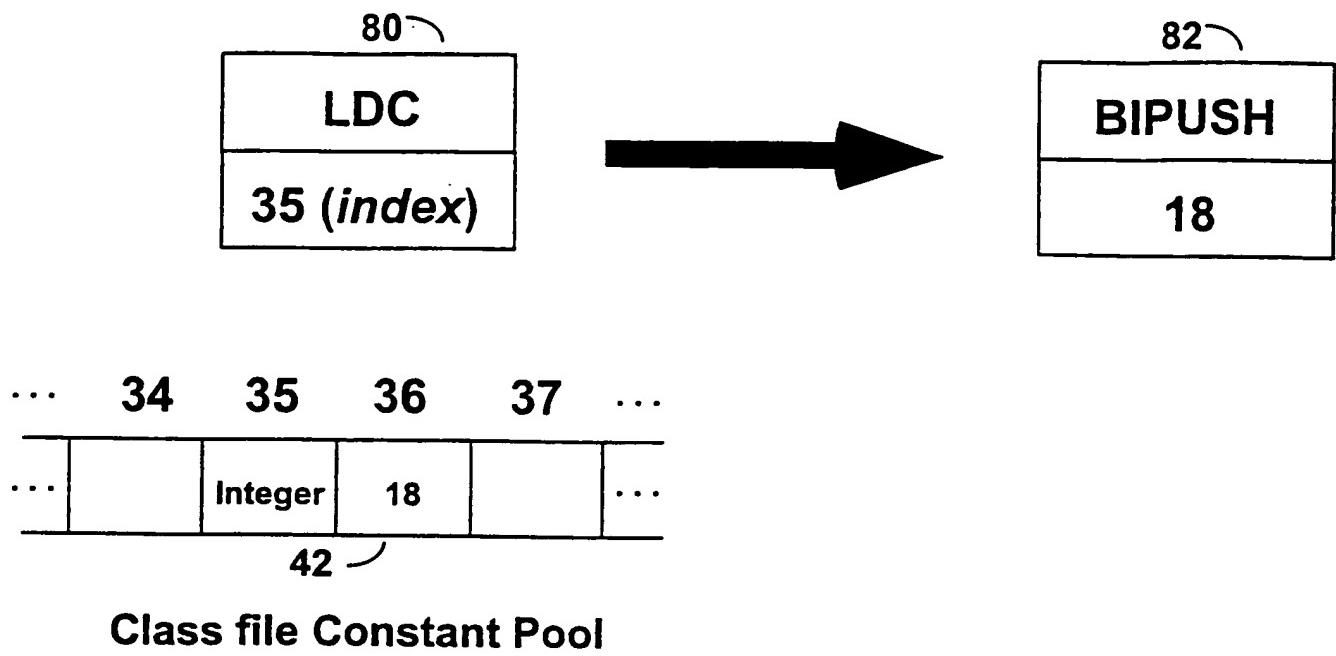
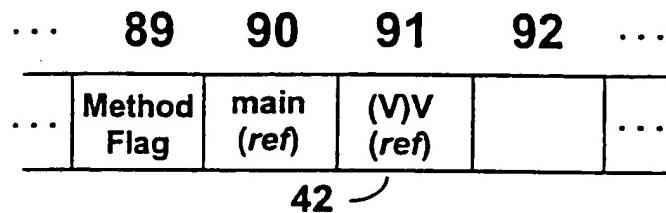
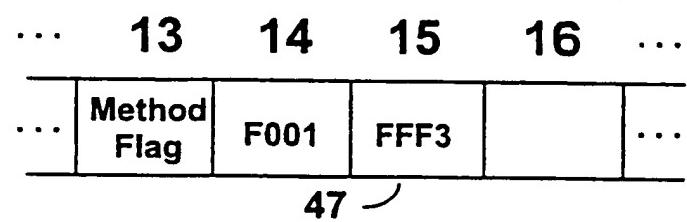
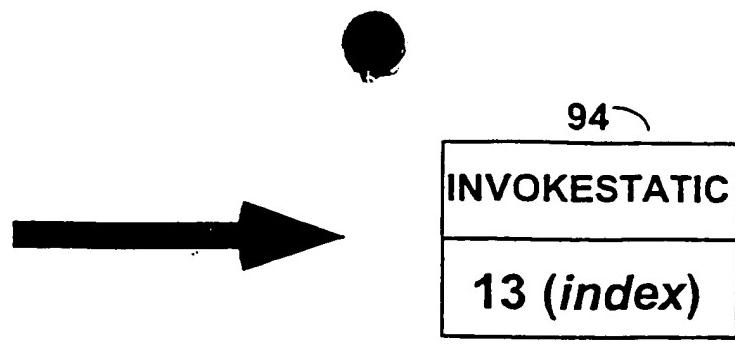


Fig. 8



Class file Constant Pool



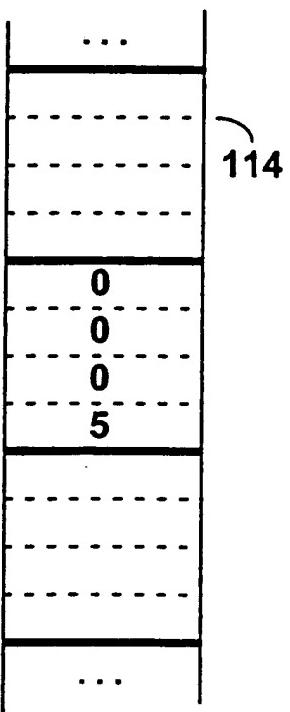
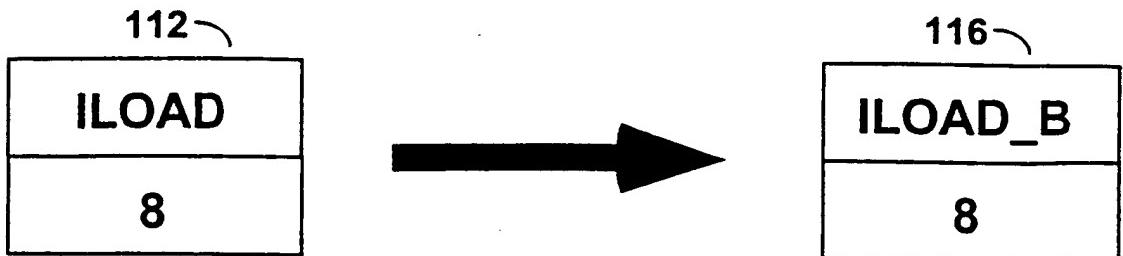
Card class file Constant Pool

Fig. 9

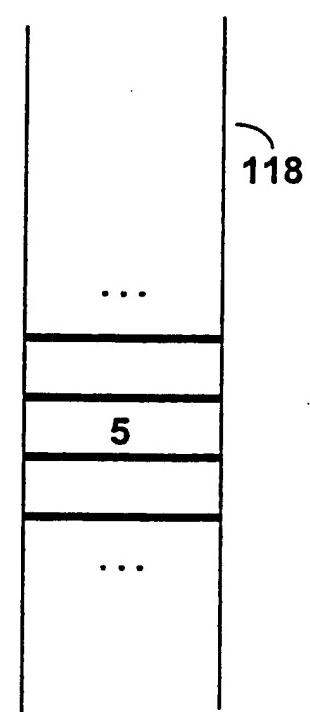
100 ↗	
0:	ALOAD 43
1:	0
2:	ILOAD 21
3:	1
4:	IFNE 154 2:
5:	BIPUSH 16
6:	5

102 ↗	
0:	ALOAD 51
1:	0
2:	ILOAD 50
3:	1
4:	IFNE 27 2:
5:	BIPUSH 49
6:	5

Fig. 10



**Word-Based Operand
Stack**



**Byte-Based Operand
Stack**

Fig. 11

Integrated Circuit Card

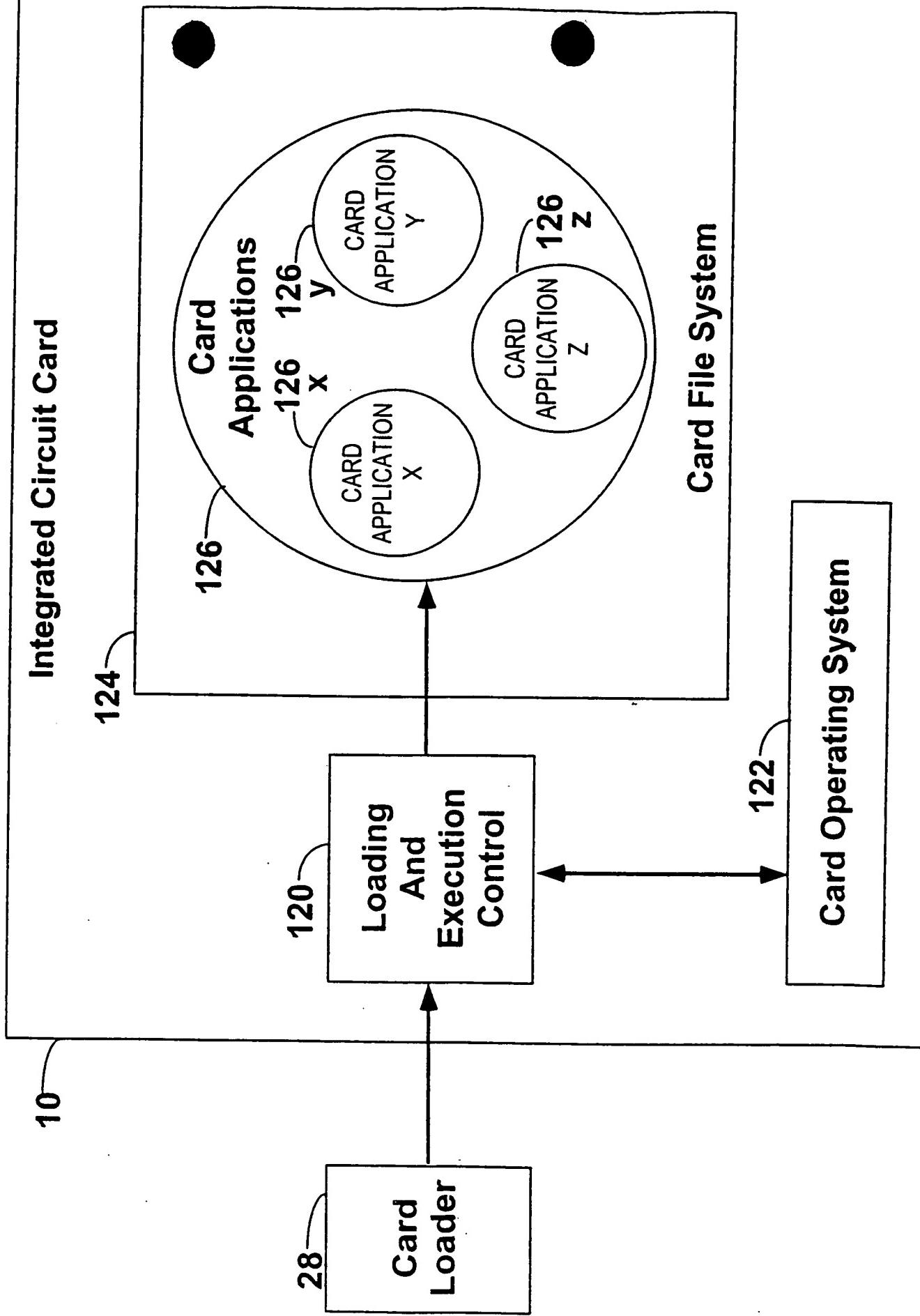
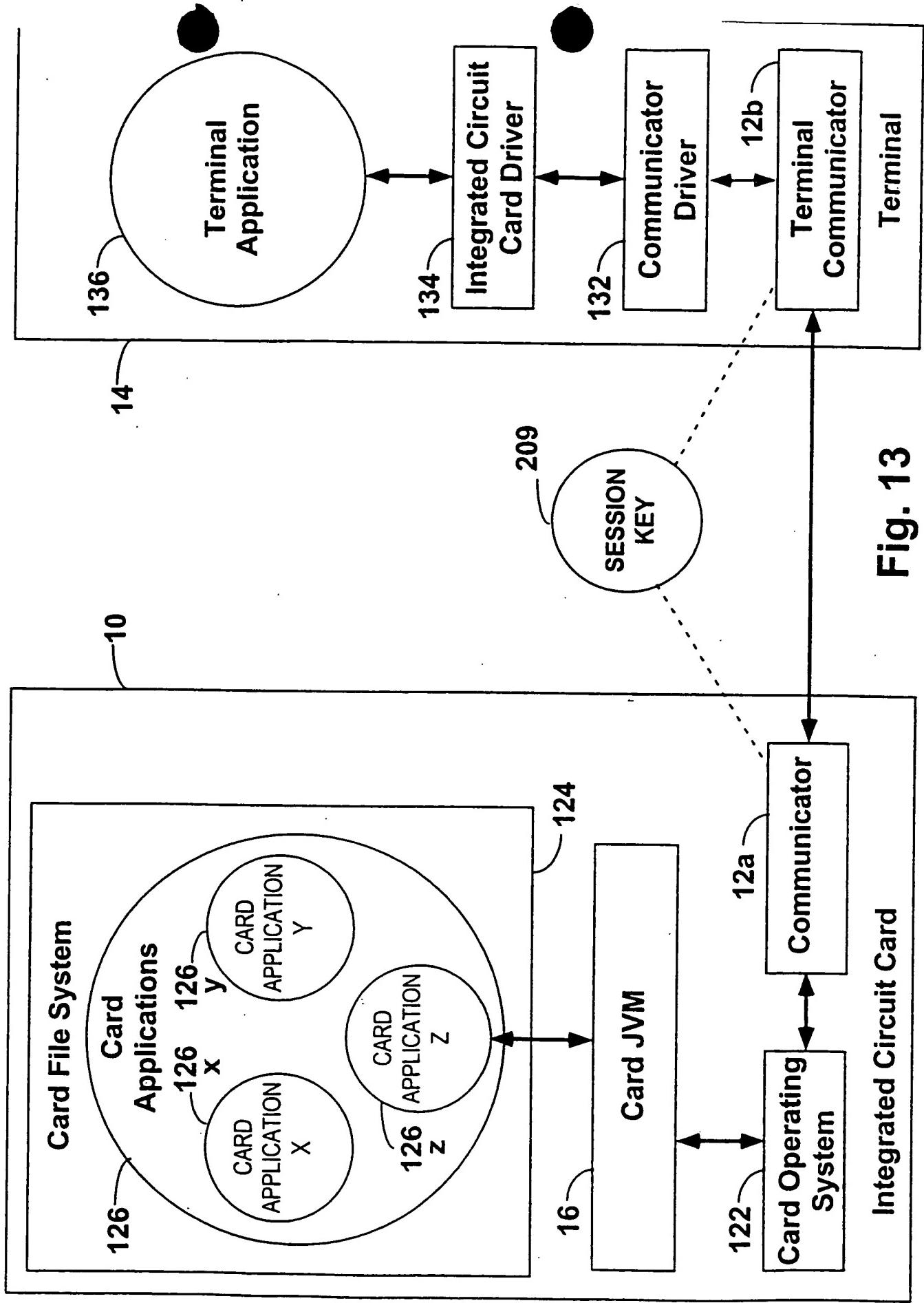


Fig. 13



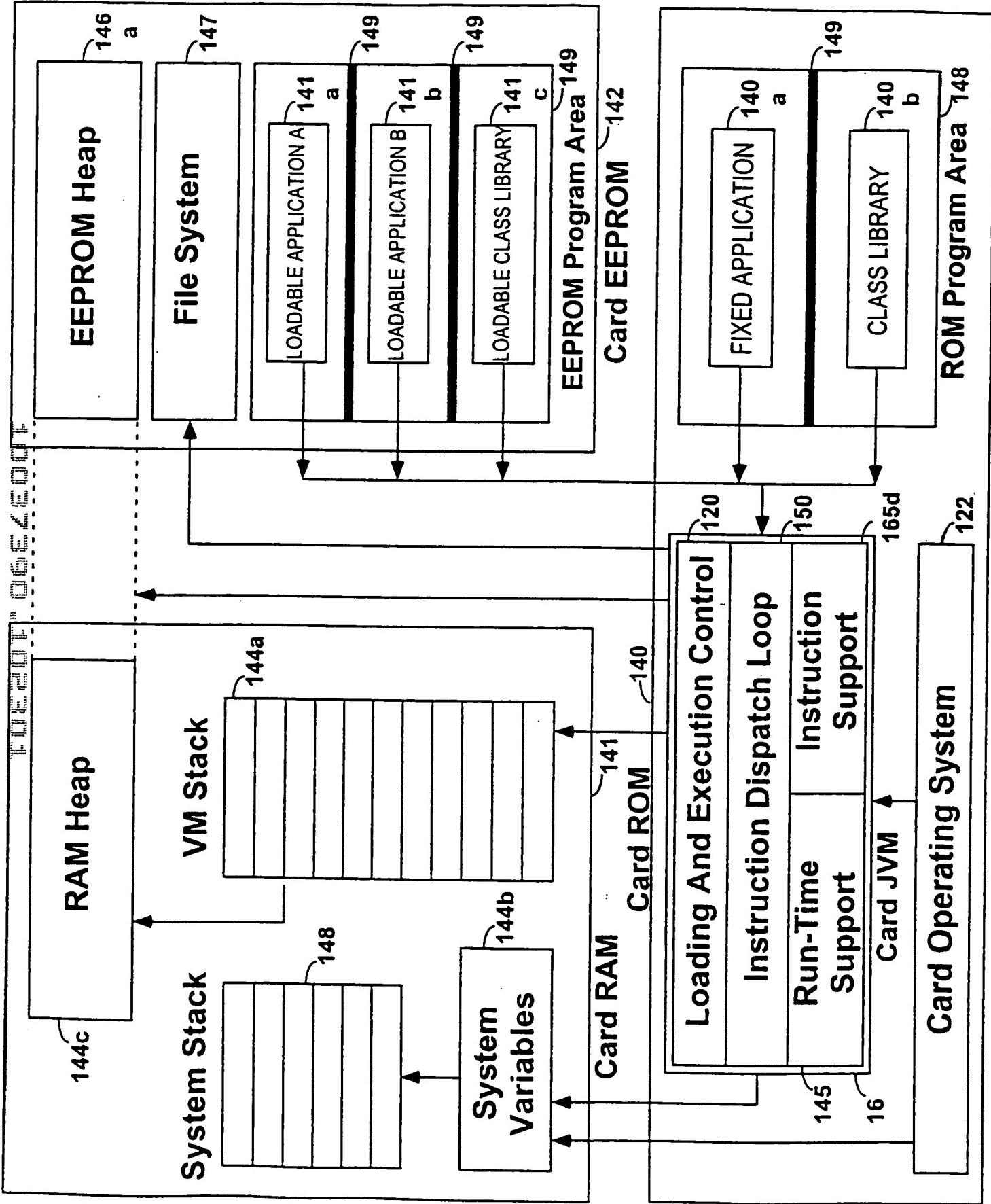


FIG. 14

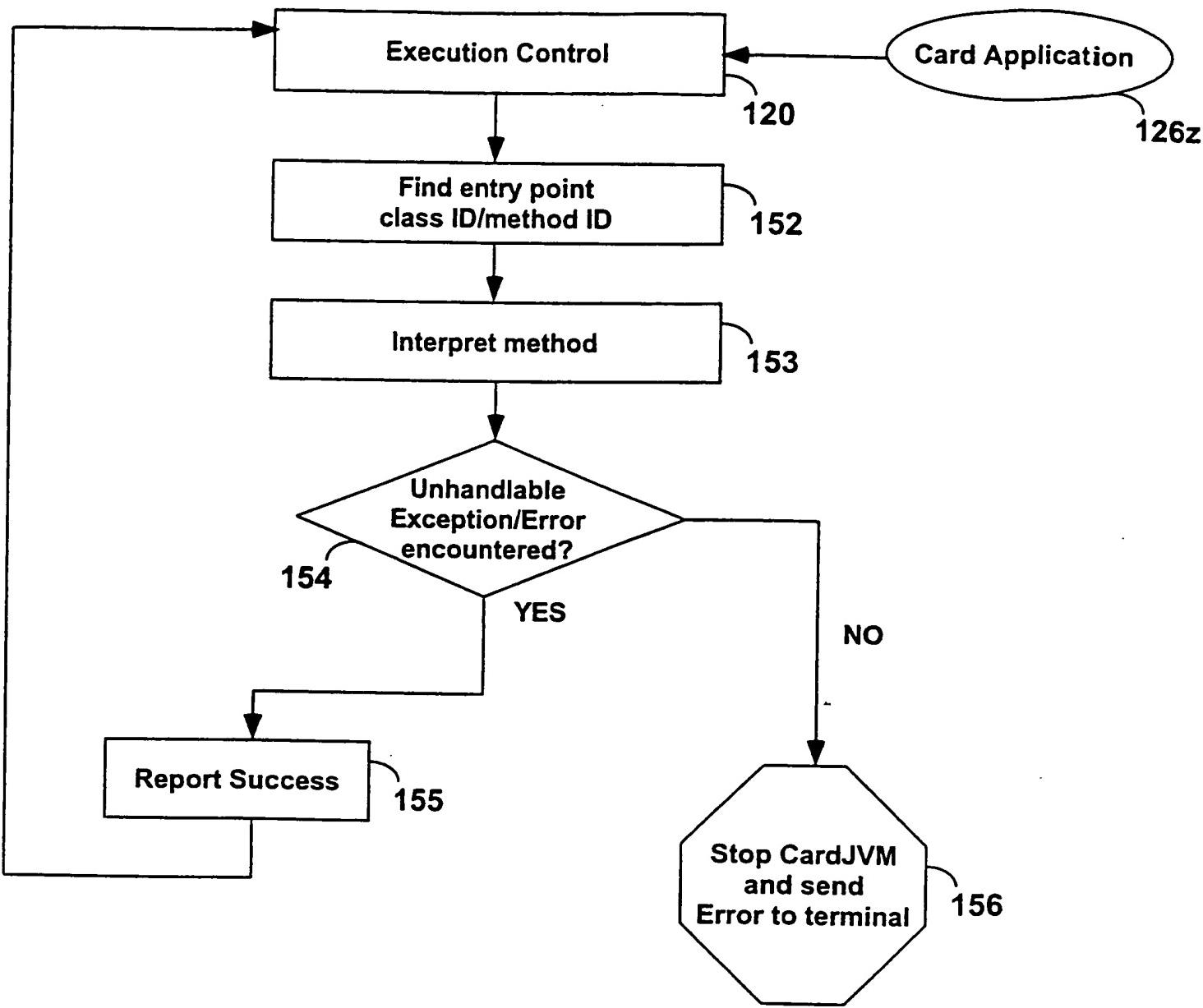


Fig. 15

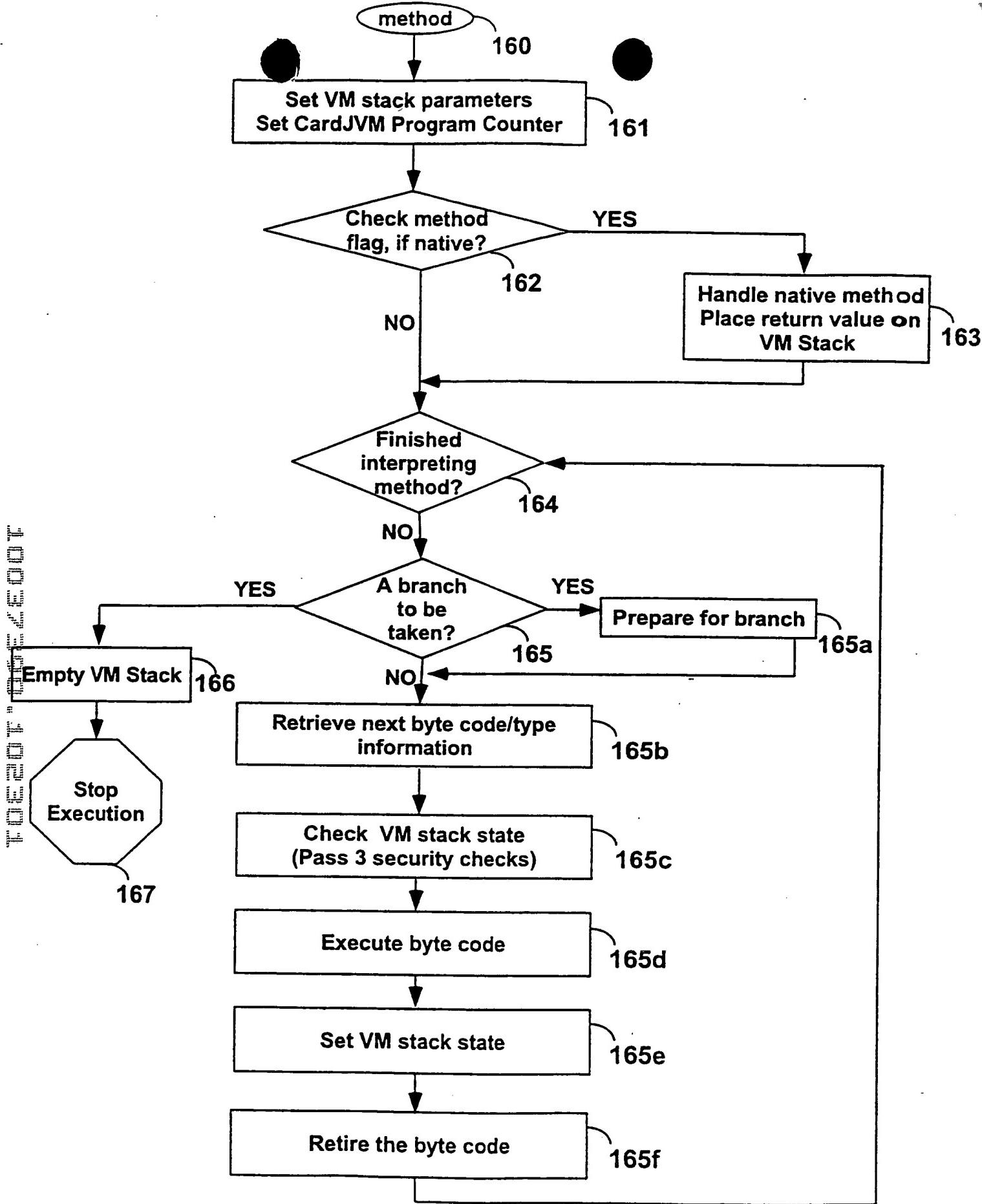


Fig. 16

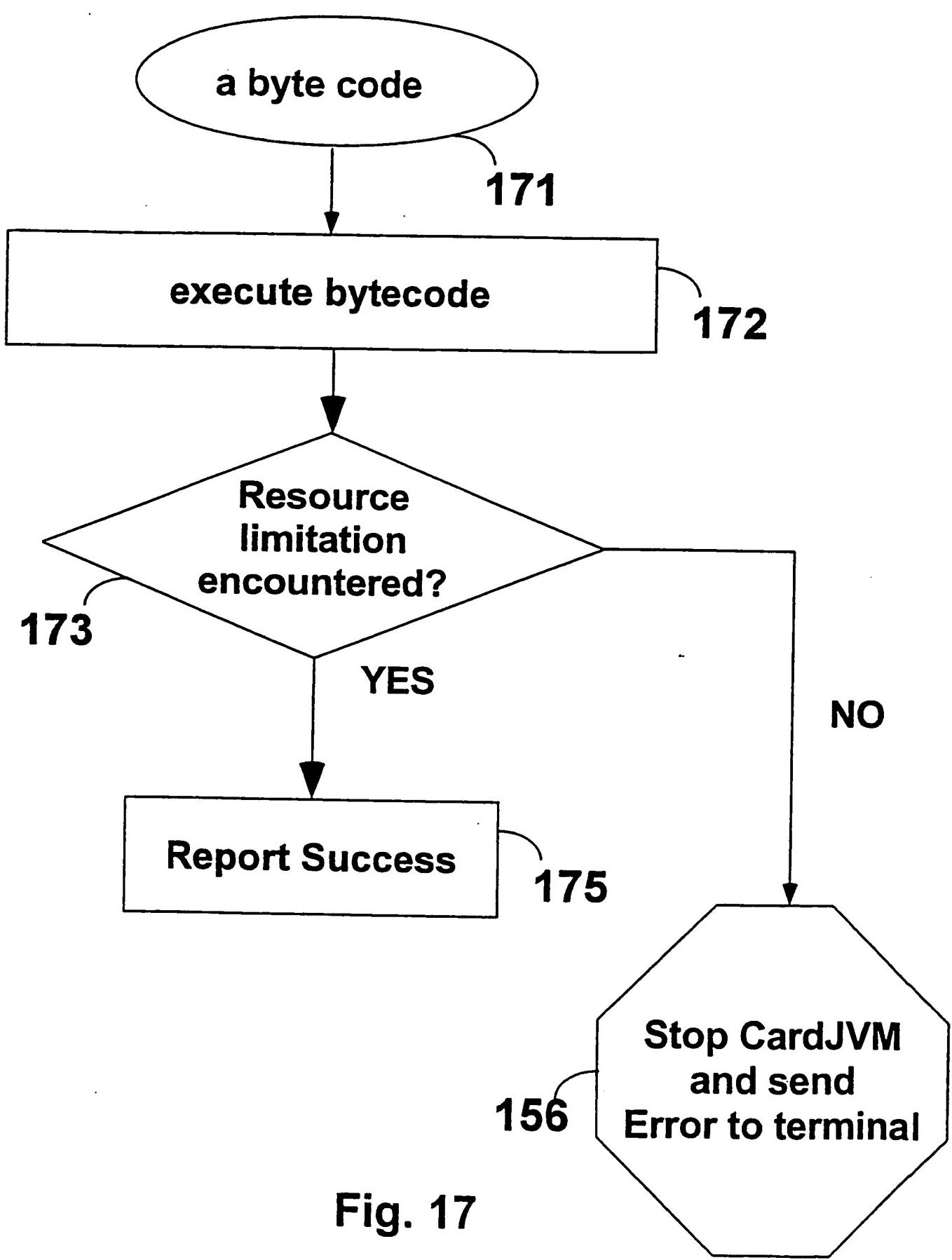


Fig. 17

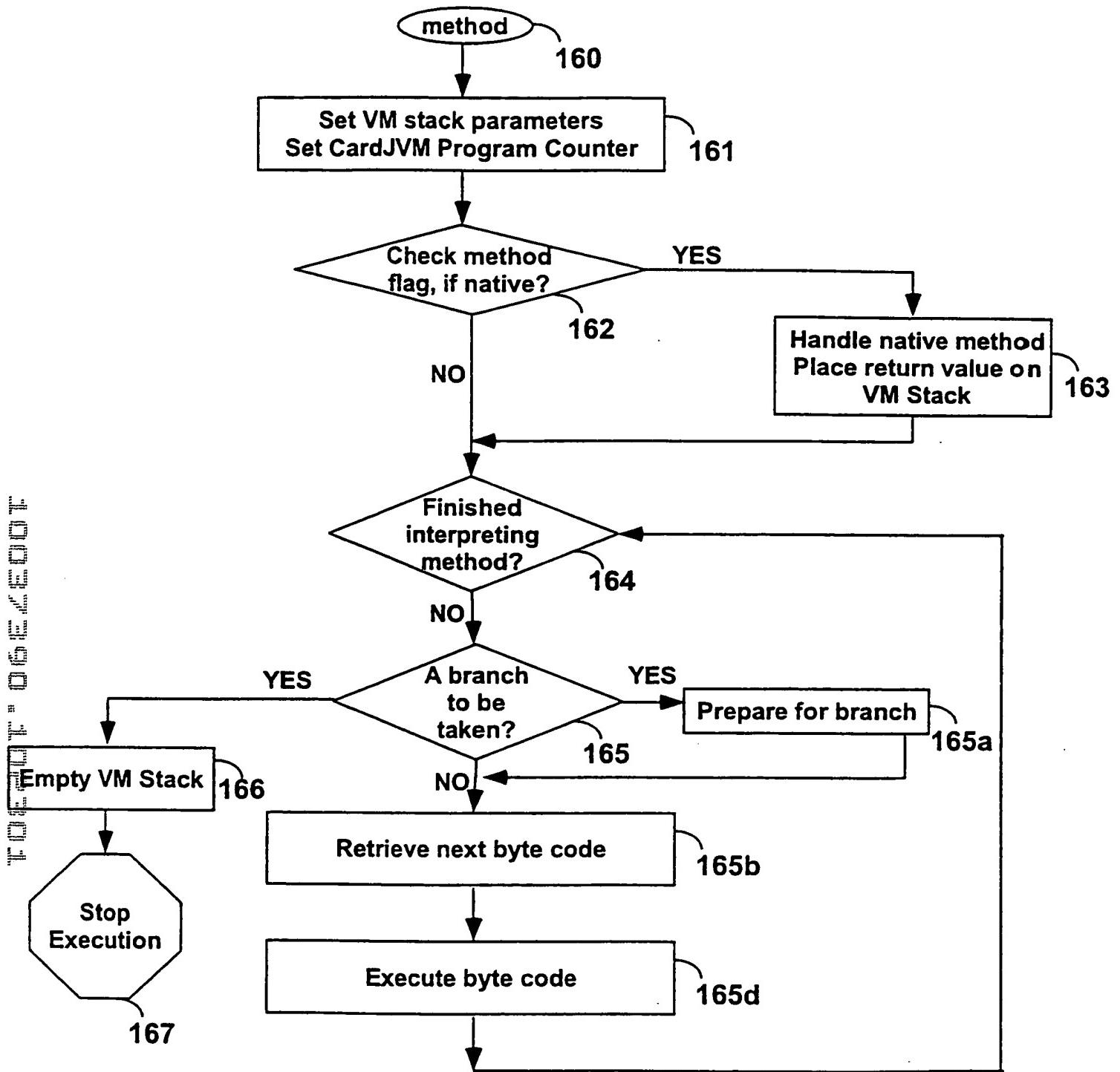


Fig. 18

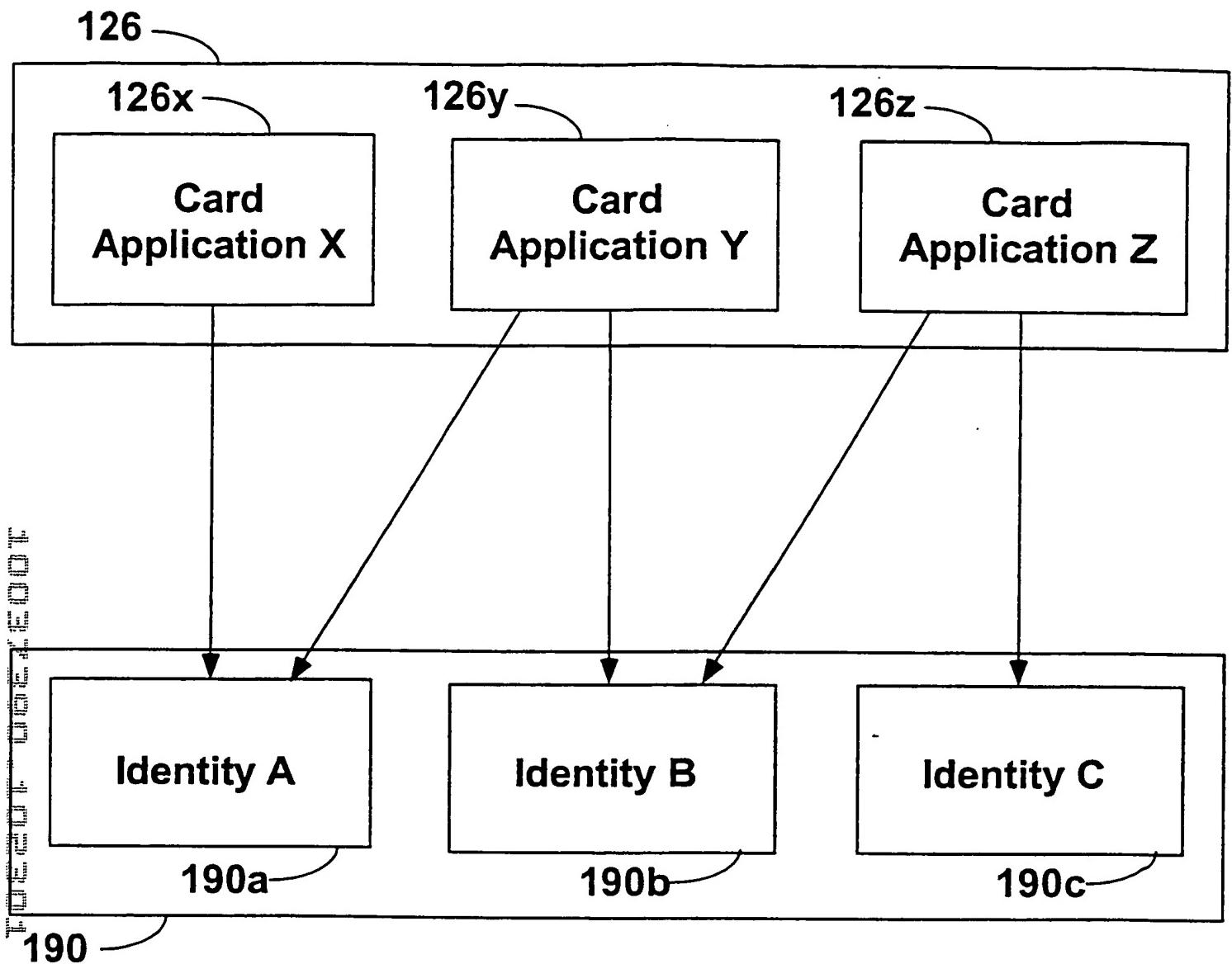


Fig. 19

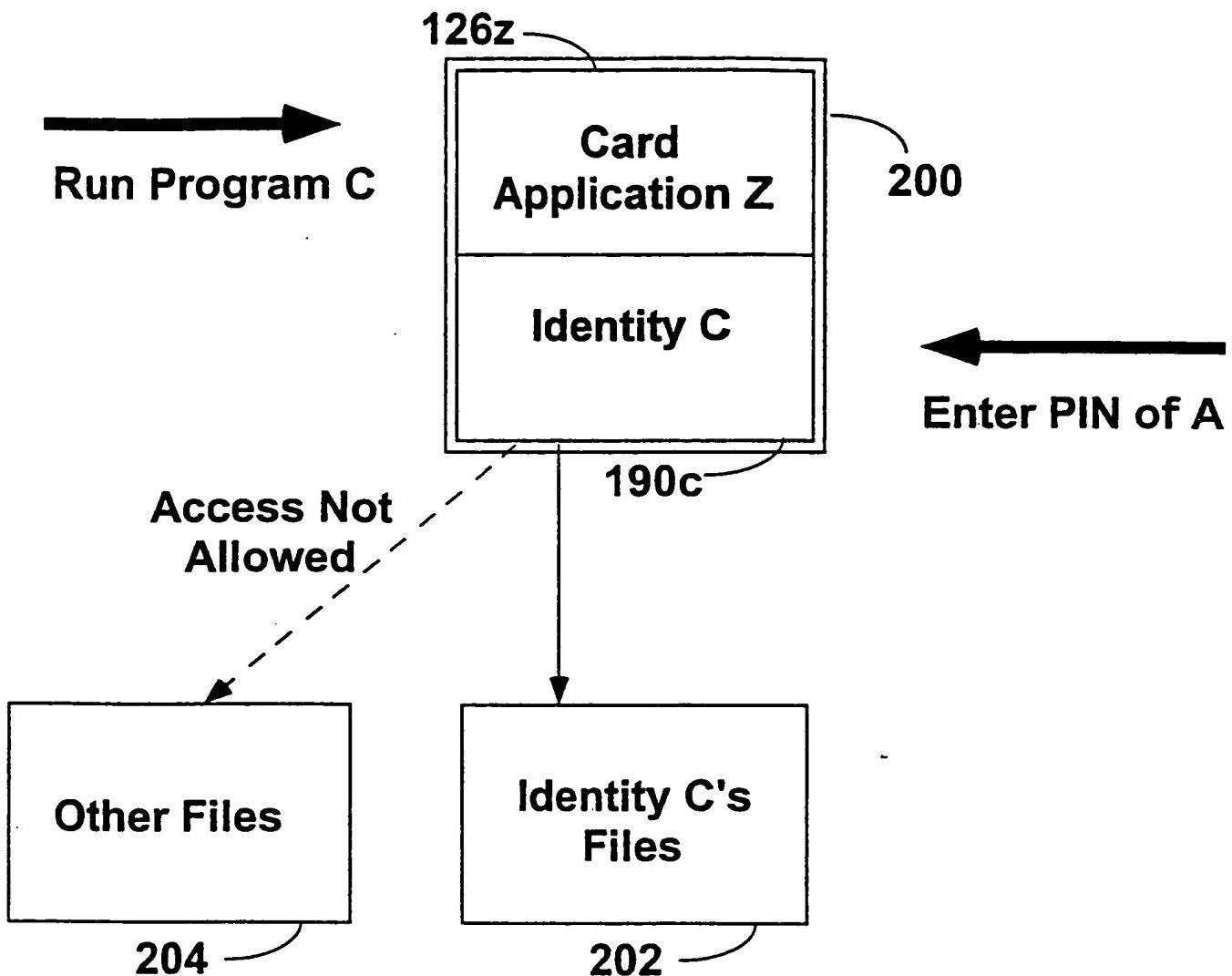


Fig. 20

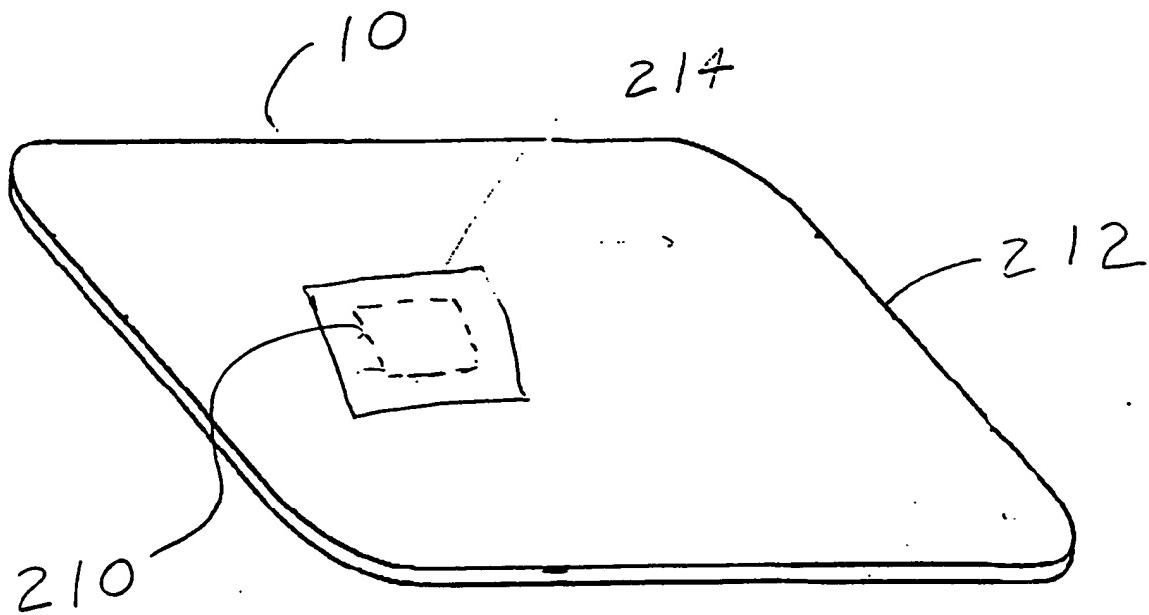


Fig. 21

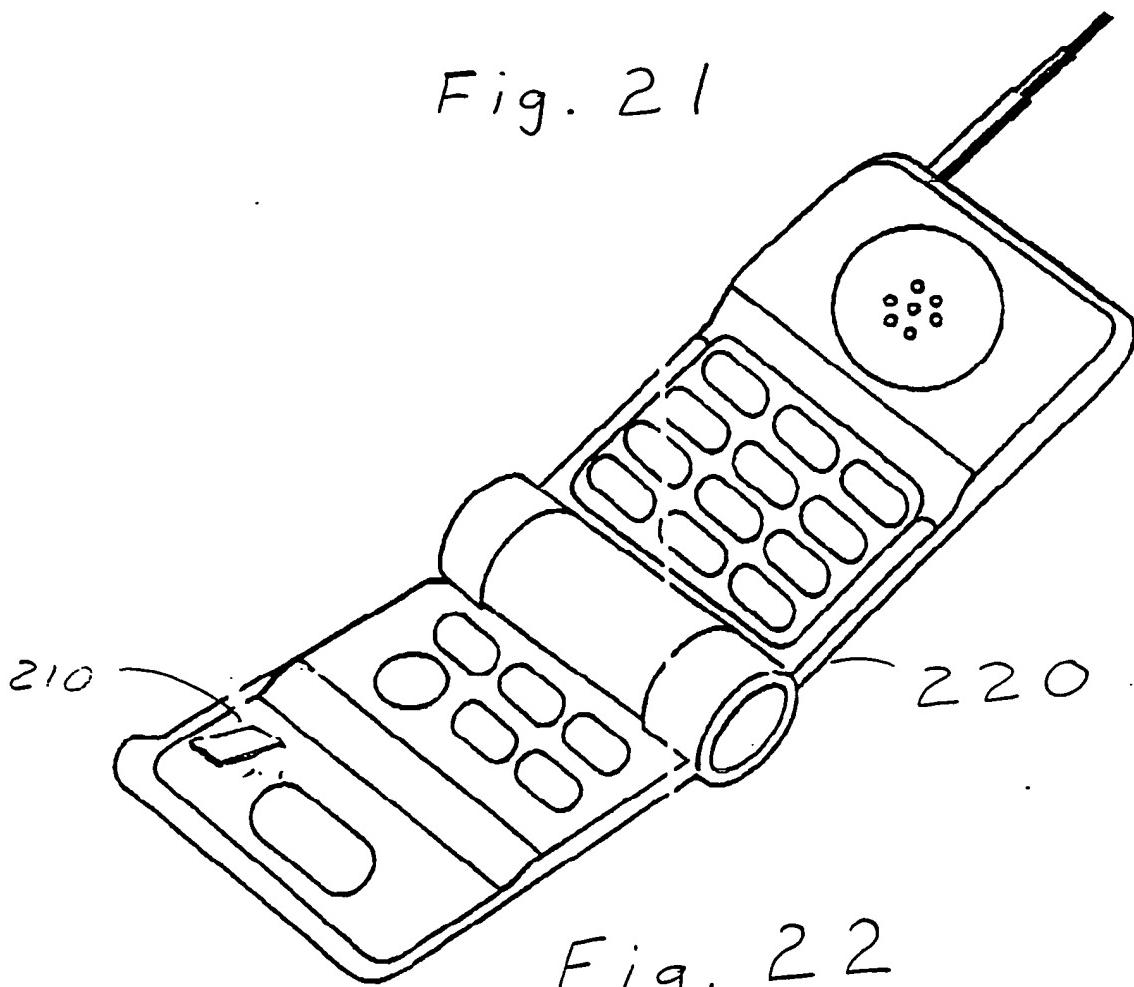


Fig. 22

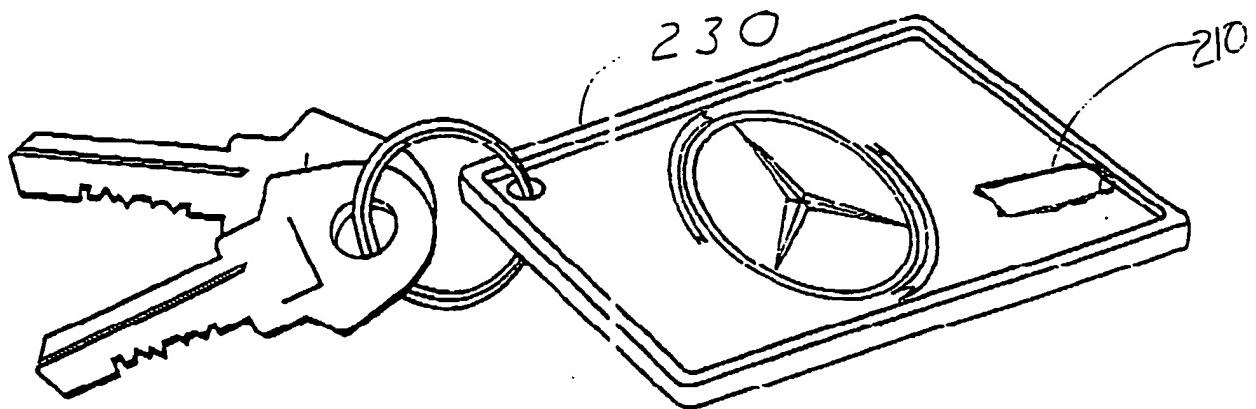


Fig. 23

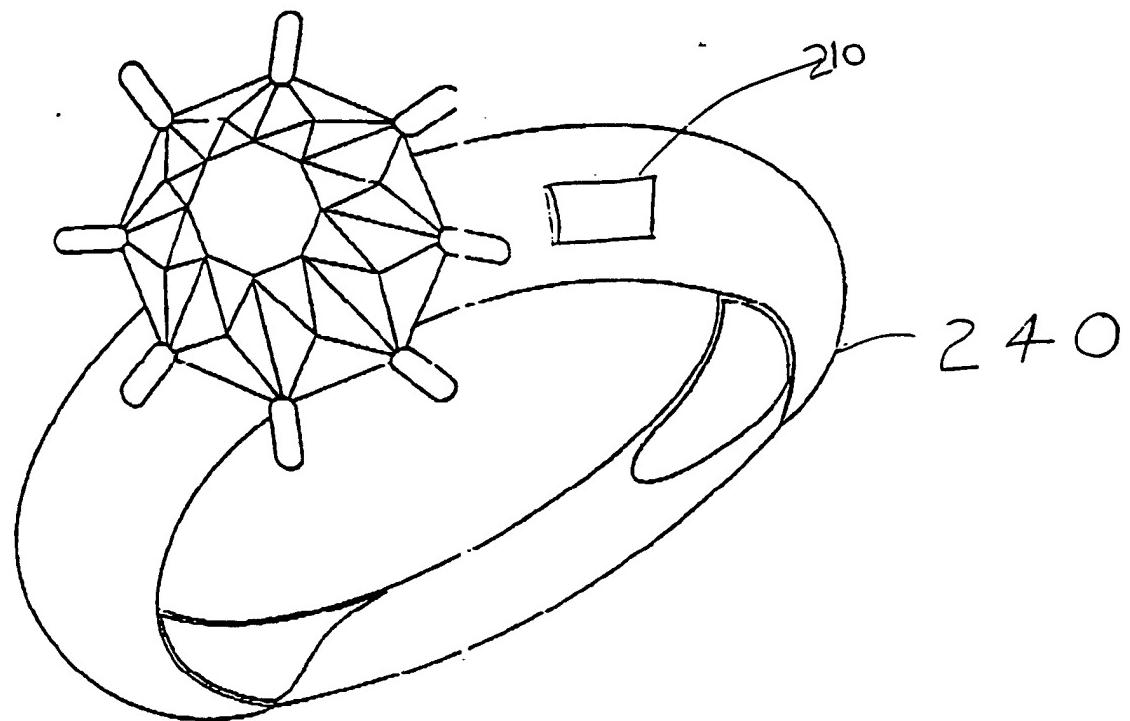


Fig. 24

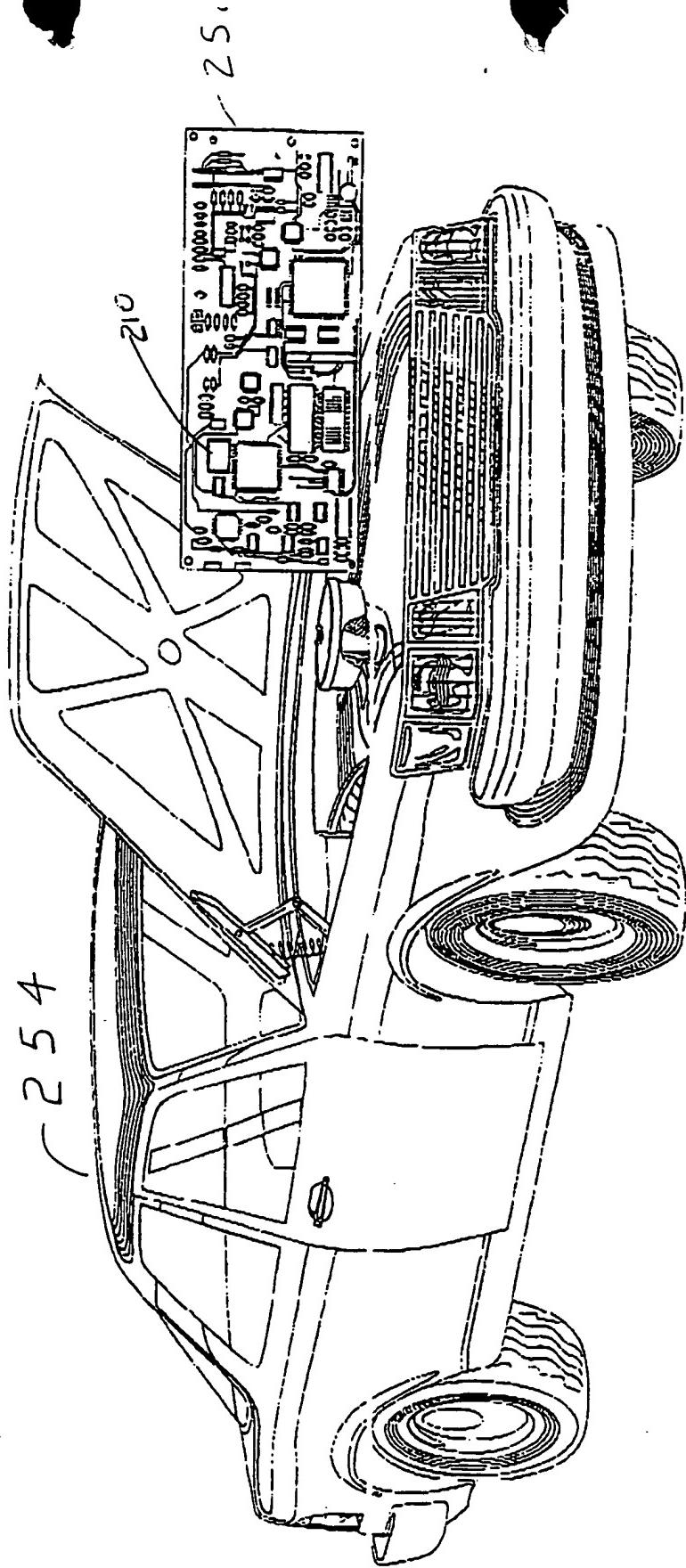


Fig. 25